

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: TOKUDA, Kazuhiko

Group Art Unit: 3729

Serial No.: 09/928,441

Examiner: CHANG, Rick Kiltae

Filed: August 14, 2001

P.T.O. Confirmation No.: 8352

FOR: PRINTED CIRCUIT BOARD WITH REDUCED CROSSTALK NOISE

AND METHOD OF FORMING WIRING LINES ON A BOARD TO

FORM SUCH A PRINTED CIRCUIT

## **AMENDMENT UNDER 37 CFR §1.111**

Commissioner for Patents Washington, D.C. 20231

December 26, 2002

Sir:

In response to the Office Action dated October 29, 2002, please amend the above-identified application as follows:

RECEIVED
DEC 3 0 2002

## **IN THE TITLE:**

**TECHNOLOGY CENTER R3700** 

Please amend the title to read as follows:

-A METHOD OF FORMING WIRING LINES ON A BOARD TO FORM

A CIRCUIT BOARD--

## **IN THE ABSTRACT**:

Delete the current Abstract and replace therewith the attached substitute Abstract.